



## **CURRICULAM VITAE**

**ABHIRAMI J S**

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Contact: 8075448115.

### **OBJECTIVE**

To be a part of a challenging and competitive organization that utilizes my technical knowledge and interpersonal skills, enriching my profile with the same.

### **ACADEMIC**

**2011-13: Govt HHS Pallikal Trivandrum Kerala**

**Plus, two -78%**

**2013-17: B. Tech in Electronics and Communication**

U K F College of Engineering and Technology **Kerala India – 7.38**

**2019-21: M. Tech in Advanced Electronics and Communication**

Rajiv Gandhi Institute of Technology Kottayam, Kerala India - 7.82

APJ ABDUL KALAM TECHNOLOGICAL UNIVERSITY

### **TECHNICAL SKILLS**

- ❖ Xilinx ISE Design Suite 14.7 ( Verilog)
- ❖ MATLAB
- ❖ Xilinx ISE Design Suite Vivado
- ❖ MS Office

### **PROJECTS**

- ❖ Digital Pulse Width Modulator (DPWM) based on phase-shifted circuit and carry chain delay line
- ❖ Performance of full adder using XOR and XNOR gates
- ❖ Home automation using raspberry pi



## SEMINAR

- ❖ FPGA based Time-to-Digital Converters
- ❖ Internet of Things for Disaster Management
- ❖ 5G Technology

## INDUSTRIAL EXPOSURE

- ❖ FPGA based design, Implementation and verification using Vivado

## PERSONAL PROFILE

Age & Date of Birth : 03 January 1996  
Passport No : S 1904444  
Marital Status : Single  
Nationality : India

## LAUNGAGES CLEAN

English, Malayalam, & Hindi

## HOBBIES & INTERESTS

Reading, Travelling, movies, music

## PERSONAL STRENGTHS

Friendly and easy going with people, keen observer, fast learner, able trainer & good listener

## REFERENCES

Up on Request

## DECLARATION

I hereby declare the above stated are true to the best of my knowledge. I shall be committed to work if I would be given an opportunity in your esteemed organization.

*Abhirami.S*

Trivandrum